

Code No.: ETEC 404
Paper: Embedded System

L T C
3 1 4

INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

UNIT – I

Introduction to an embedded systems design & RTOS: Introduction to Embedded system, Processor in the System, Microcontroller, Memory Devices, Embedded System Project Management, ESD and Co-design issues in System development Process, Design cycle in the development phase for an embedded system, Use of target system or its emulator and In-circuit emulator, Use of software tools for development of an ES.

Inter-process Communication and Synchronization of Processes, Tasks and Threads, Problem of Sharing Data by Multiple Tasks, Real Time Operating Systems: OS Services, I/O Subsystems, Interrupt Routines in RTOS Environment, RTOS Task Scheduling model, Interrupt Latency and Response times of the tasks.

[No. of Hrs.: 11]

UNIT – II

Overview of Microcontroller: Microcontroller and Embedded Processors, Overview of 8051 Microcontroller family: Architecture, basic assembly language programming concepts, The program Counter and ROM Spaces in the 8051, Data types, 8051 Flag Bits and PSW Register, 8051 Register Banks and Stack Instruction set, Loop and Jump Instructions, Call Instructions, Time delay generations and calculations, I/O port programming Addressing Modes, accessing memory using various addressing modes, Arithmetic instructions and programs, Logical instructions, BCD and ASCII application programs, Single-bit instruction programming, Reading input pins vs. port Latch, Programming of 8051 Timers, Counter Programming

[No. of Hrs.: 11]

UNIT – III

Communication with 8051: Basics of Communication, Overview of RS-232, I²C Bus, UART, USB, 8051 connections to RS-232, 8051 serial communication programming, 8051 interrupts, Programming of timer interrupts, Programming of External hardware interrupts, Programming of the serial communication interrupts, Interrupt priority in the 8051 **[No. of Hrs.: 11]**

UNIT - IV

Interfacing with 8051: Interfacing an LCD to the 8051, 8051 interfacing to ADC, Sensors, Interfacing a Stepper Motor, 8051 interfacing to the keyboard, Interfacing a DAC to the 8051, 8255 Interfacing with 8031/51, 8051/31 interfacing to external memory

[No. of Hrs.: 11]

TEXT BOOKS:

1. Raj Kamal, “Embedded Systems”, TMH, 2004.
2. M.A. Mazidi and J. G. Mazidi, “The 8051 Microcontroller and Embedded Systems”, PHI, 2004.

REFERENCES BOOKS:

1. David E. Simon, “An Embedded Software Primer”, Pearson Education, 1999.
2. K.J. Ayala, “The 8051 Microcontroller”, Penram International, 1991.
3. Dr. Rajiv Kapadia, “8051 Microcontroller & Embedded Systems”, Jaico Press

4. Dr. Prasad, "Embedded Real Time System", Wiley Dreamtech, 2004.