

Code No.: ETIT 308
Paper: Digital Signal Processing

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INSTRUCTIONS TO PAPER SETTERS:

MAXIMUM MARKS: 75

1. Question No. 1 should be compulsory and cover the entire syllabus. This question should have objective or short answer type questions. It should be of 25 marks.
2. Apart from question no. 1, rest of the paper shall consist of four units as per the syllabus. Every unit should have two questions. However, student may be asked to attempt only 1 question from each unit. Each question should be of 12.5 marks.

UNIT – I

Discrete time signals and systems, Z-transforms, structures for digital filters, design procedures for FIR and IIR filters. Frequency transformations: linear phase design; DFT. Methods for computing FFT. Noise analysis of digital filters, power spectrum estimation.

Signals and signal Processing: characterization & classification of signals, typical Signal Processing operations, example of typical Signals, typical Signals Processing applications.

Time Domain Representation of Signals & Systems: Discrete Time Signals, Operations on Sequences, the sampling process, Discrete-Time systems, Time-Domain characterization of LTI Discrete-Time systems, state-space representation of LTI Discrete-Time systems, random signals.

[No. of Hrs.: 11]

UNIT – II

Transform-Domain Representation of Signals: the Discrete-Time Fourier Transform, Discrete Fourier Transform, DFT properties, computation of the DFT of real sequences, Linear Convolution using the DFT. Z-transforms, Inverse z-transform, properties of z-transform, transform domain representations of random signals, FFT Algorithms.

[No. of Hrs.: 11]

UNIT – III

Transform-Domain Representation of LTI Systems: the frequency response, the transfer function, types of transfer function, minimum-phase and maximum-Phase transfer functions, complementary transfer functions, Discrete-Time processing of random signals.

Digital Processing of Continuous-Time Signals : sampling of Continuous Signals, Analog Filter Design, Anti-aliasing Filter Design, Sample-and-hold circuits, A/D & D/A converter, Reconstruction Filter Design.

[No. of Hrs.: 11]

UNIT – IV

Digital Filter Structure: Block Diagram representation, Signal Flow Graph Representation, Equivalent Structures, FIR Digital Filter Structures, IIR Filter Structures, Parallel all pass realization of IIR transfer function, Digital Sine-Cosine generator.

Digital Filter Design: Impulse invariance method of IIR filter design, Bilinear Transform method of IIR Filter Design, Design of Digital IIR notch filters, FIR filter Design based on truncated Fourier series, FIR filter design based on Frequency Sampling approach.

Applications of DSP.

[No. of Hrs.: 11]

TEXT BOOKS:

1. Sanjit K. Mitra, “DSP a Computer based approach” , TMH, 2nd Ed., 2001.
2. Allan Y. Oppenheim & Ronald W. Schacter , "Digital Signal Processing", PHI, 2004.

REFERENCE BOOKS:

1. J. R. Johnson, “Introduction to Digital Signal Processing”, PHI, 2000.

2. B. Somanthan Nair, "Digital Signal Processing: Theory, Analysis & Digital Filter Design", PHI, 2004.