

Q1. What is the difference between near & far procedures of 8086 processor? Explain.

Q2. Draw & discuss flag registers of 8086.

Q3. Construct the Machine code :-

MOV CS: [BX] DL

opcode for MOV \rightarrow 100010.

Q4. Explain with suitable diagram how 8086 access a byte or word from even & odd memory banks.

Q5. Explain following :-

- (i) Pipelining in 8086.
- (ii) Segmented memory.
- (iii) REP prefix.
- (iv) Effective address

Q6. Justify the statement "Programs in 8086 micro-processor are relocatable".

Submission on

13/2/15